

101

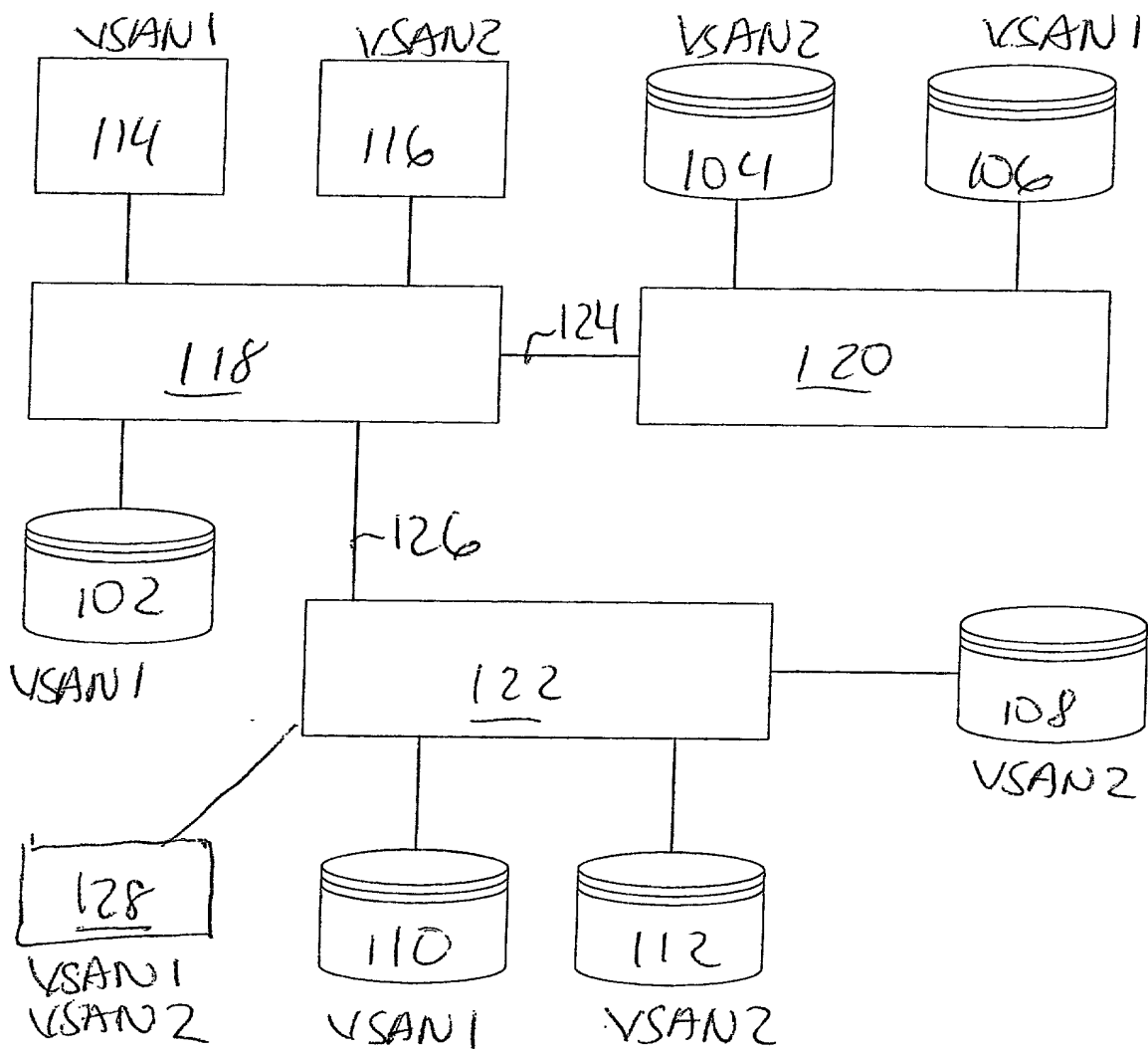


FIG. 1

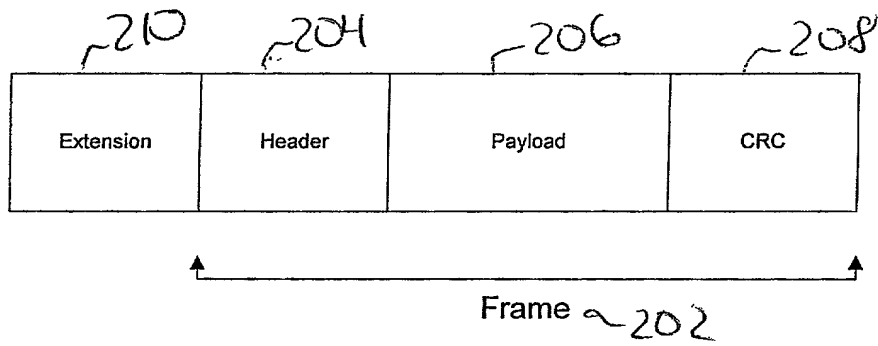


FIG. 2

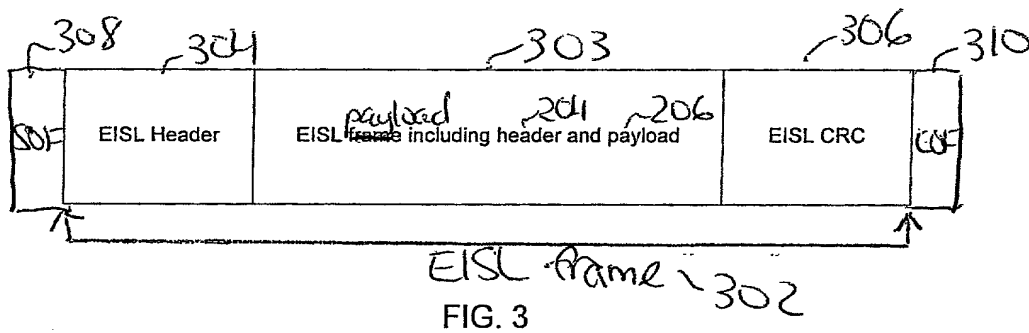


FIG. 3

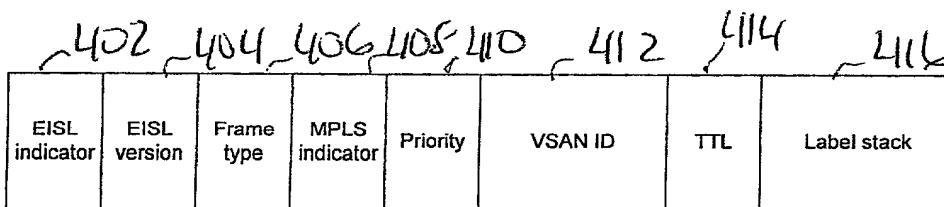


FIG. 4

502	504	506	508
Label	Exp	S	TTL

FIG. 5

FIG. 6 is a block diagram of a network switch architecture.

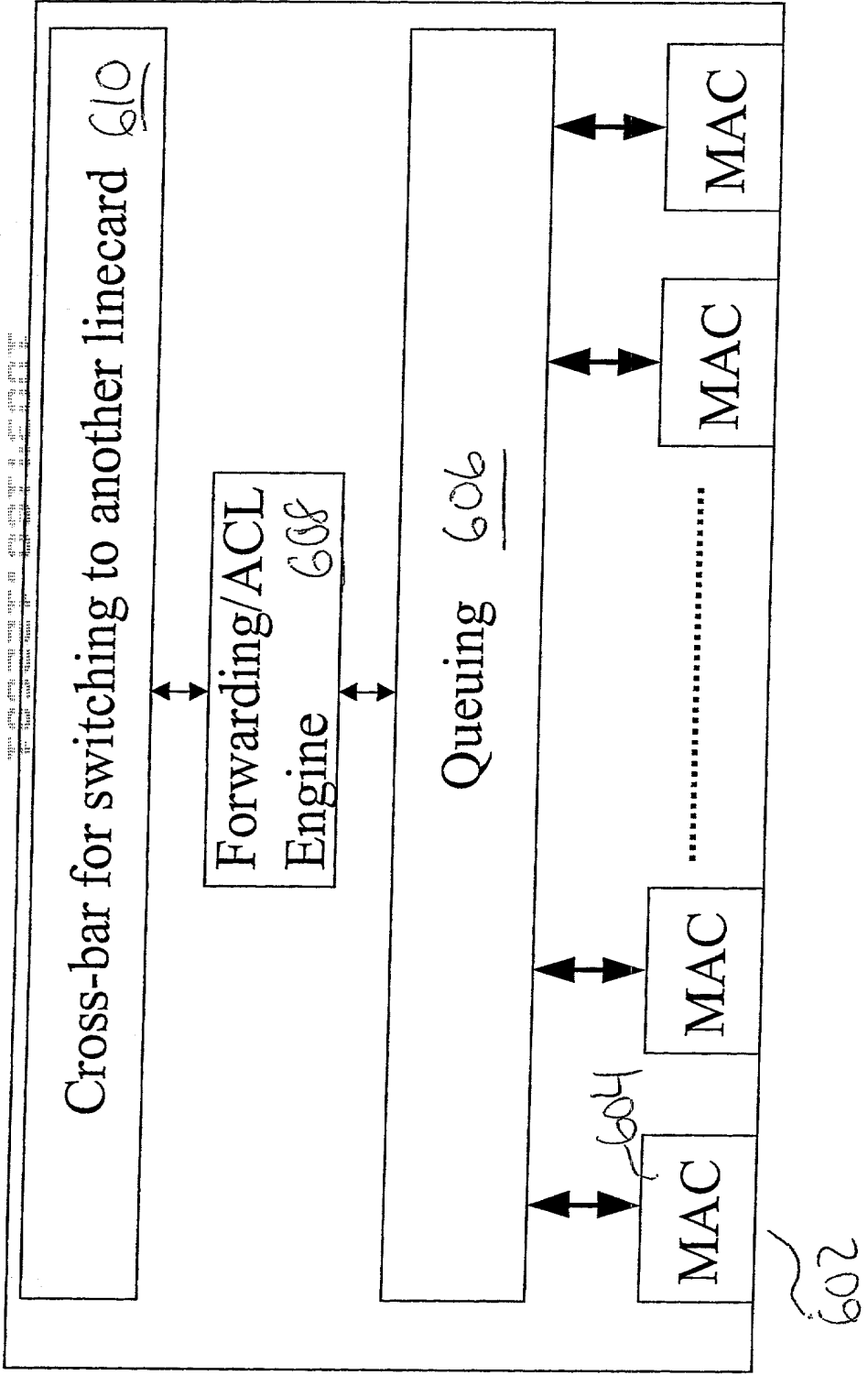


FIG. 6